L Number	Hits	Search Text	DB	Time stamp
1	0	ICE and debug\$3 and microcontroller and nguyen-ba.xp.	USPAT; US-PGPUB; EPO; JPO;	2004/08/13 15:14
2	24	717/124-135	DERWENT; IBM_TDB USPAT;	2004/08/13
3	1566	(717/124-135).CCLS.	US-PGPUB USPAT;	15:14 2004/08/13
4	0	ICE and debug\$3 and nguyen-ba.xa.	US-PGPUB USPAT;	15:15 2004/08/13
			US-PGPUB; EPO; JPO; DERWENT; IBM TDB	15:15
5	528	ICE and debug\$3	USPAT; US-PGPUB; EPO; JPO;	2004/08/13 15:15
6	2	(ICE and debug\$3 ) and nguyen-ba.xp.	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2004/08/13 15:17
7	0	(in adj circuit adj emulation) and debug\$3 and nguyen-ba.xp.	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2004/08/13 15:18
8	0	(in adj circuit adj emulation) and debug\$3 and nguyen-ba.xa.	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2004/08/13 15:19
9	0	(in adj circuit adj emulation) and nguyen-ba.xa.	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2004/08/13
10	20	interface and debug\$3 and nguyen-ba.xa.	DERWENT; IBM_TDB USPAT; US-PGPUB;	2004/08/13
			EPO; JPO; DERWENT; IBM_TDB USPAT;	2004/08/13
11	64	jtag and nguyen-ba.xa. or nguyen-ba.xp.	US-PGPUB; EPO; JPO;	15:29
12	19	(jtag and nguyen-ba.xa. or nguyen-ba.xp.) and debug\$3	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2004/08/13 15:25
13	45	(jtag and nguyen-ba.xa. or nguyen-ba.xp.) not ((jtag and nguyen-ba.xa. or nguyen-ba.xp.) and debug\$3)	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2004/08/13 15:25
14	0	jtag and (nguyen-ba.xa. or nguyen-ba.xp.)	DERWENT; IBM_TDB USPAT; USPAT;	2004/08/13 15:29
15	3137	jtag	EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/08/13 15:29
			IBM TDB	

110		Tetra de la companya del companya de la companya de la companya del companya de la companya de l	USPAT;	2004/08/13
16	0	jtag and nguyen-ba.xa.	US-PGPUB;	15:29
	ı		EPO; JPO;	15.25
			DERWENT;	
1	į.		IBM_TDB	1
17	0	jtag and nguyen-ba.xp.	USPAT;	2004/08/13
1	1		US-PGPUB;	15:30
			EPO; JPO; DERWENT;	!
	[		IBM TDB	1
18	1001	   jtag and debug\$3	USPAT;	2004/08/13
10	1001		US-PGPUB;	15:30
1	ļ		EPO; JPO;	
		•	DERWENT;	1
10		1 545 4104 425	IBM_TDB	
19	0	(jtag and debug\$3) and 717/124-135	USPAT;	2004/08/13
	<b>!</b> •		US-PGPUB; EPO; JPO;	15:31
			DERWENT;	ŀ
	1		IBM TDB	[
21	0	((jtag and debug\$3) and	USPAT;	2004/08/13
		717/124-135.ccls.) and nguyen-ba.xa.	US-PGPUB;	15:31
1	ŀ		EPO; JPO;	}
	1		DERWENT;	
22	0	(/itag and dobuges) and	IBM_TDB USPAT;	2004/09/12
44		((jtag and debug\$3) and 717/124-135.ccls.) and nguyen-ba.xp.	US-PGPUB;	2004/08/13 15:31
i	1	'1'/ 124 130.0013./ and nguyen-ba.xp.	EPO; JPO;	12.21
[			DERWENT;	
Ì	Ì		IBM TDB	1
23	0	( )	USPĀT;	2004/08/13
		717/124-135.ccls.) and "nguyen ba".xp.	US-PGPUB;	15:31
ļ	}		EPO; JPO;	}
			DERWENT;	
20	57	   (jtag and debug\$3) and 717/124-135.ccls.	IBM_TDB USPAT;	2004/08/13
20	37	() tag and debug\$5/ and /1//124-155.ccis.	US-PGPUB;	15:33
1			EPO; JPO;	13.33
	ļ		DERWENT;	
į į	Ì		IBM TDB	1
24	1	ICE with microcontroll\$3 with halt\$3	USPAT;	2004/08/13
[		,	US-PGPUB;	15:34
]	1		EPO; JPO;	J J
	ļ		DERWENT; IBM TDB	
25	) 0	emulat\$3 with microcontroll\$3 with halt\$3	USPAT;	2004/08/13
		The state of the s	US-PGPUB;	15:35
1	1		EPO; JPO;	
	<b>,</b>		DERWENT;	
26	_		IBM_TDB	0004/00/12
26	3	debug\$3 with microcontroll\$3 with halt\$3	USPAT;	2004/08/13
] .	[		US-PGPUB; EPO; JPO;	15:38
		** ***	DERWENT;	
			IBM TDB	
27	0	ICE and debug\$3 near (microcontroller adj	USPAT;	2004/08/13
	1	code)	US-PGPUB;	15:39
	1		EPO; JPO;	
ļ	1		DERWENT;	
28	5	   debug\$3 near (microcontroller adj code)	IBM_TDB	2004/08/13
] = "			USPAT; US-PGPUB;	15:40
			EPO; JPO;	13.10
] .		· *	DERWENT;	
			IBM_TDB	
29	101	microcontroller adj code	USPAT;	2004/08/13
			US-PGPUB;	15:40
	}		EPO; JPO;	
			DERWENT;	
		<u> </u>	IBM TDB	L

	201	(microcontroller adj code) and debug\$3	USPAT;	2004/08/13
30	29	(microcontroller adj code) and debugys	US-PGPUB;	15:41
	İ	·	EPO; JPO;	
	ļ		DERWENT; TBM TDB	
21	21423033	((microcontroller adj code) and debug\$3)	USPAT;	2004/08/13
31	21423033	@ad<20011115	US-PGPUB;	15:41
			EPO; JPO;	
			DERWENT; IBM TDB	
	2.1	((microcontroller adj code) and debug\$3)	USPAT;	2004/08/13
32	21	and @ad<20011115	US-PGPUB;	15:45
		did Gdd 120011110	EPO; JPO;	
			DERWENT;	
			IBM_TDB USPAT;	2004/08/13
33	1992	halt\$3 near state	US-PGPUB;	15:45
			EPO; JPO;	
		,	DERWENT;	
			IBM_TDB USPAT;	2004/08/13
34	3		US-PGPUB;	15:54
		adj emulation)	EPO; JPO;	
			DERWENT;	
			IBM_TDB	2004/08/13
35	8	virtual adj microcontroller	USPAT; US-PGPUB;	16:25
			EPO; JPO;	10.20
			DERWENT;	
			IBM_TDB	0004/00/12
36	1425	(703/23-28).CCLS.	USPAT; US-PGPUB;	2004/08/13
			EPO; JPO;	10.20
			DERWENT;	
			IBM_TDB	1
37	989	(714/30-31,34).CCLS.	USPAT;	2004/08/13
		·	US-PGPUB; EPO; JPO;	16:27
			DERWENT;	
	ļ		IBM_TDB	
38	2389		USPAT;	2004/08/13
		((714/30-31,34).CCLS.)	US-PGPUB; EPO; JPO;	16,27
			DERWENT;	
			IBM_TDB	
39	2181		USPAT;	2004/08/13
		((714/30-31,34).CCLS.)) and @ad<20011115	US-PGPUB; EPO; JPO;	16:27
			DERWENT;	
	-		IBM_TDB	0004/00/20
40	2		USPAT;	2004/08/13 16:30
		(microcontroller adj code)	US-PGPUB; EPO; JPO;	10.30
			DERWENT;	
			IBM_TDB	
41	6	debug\$3 with (microcontroller adj code)	USPAT;	2004/08/13
			US-PGPUB; EPO; JPO;	16:30
			DERWENT;	
			IBM_TDB	
42	2	(emulat\$3 with debug\$3 with	USPAT;	2004/08/13
		(microcontroller adj code)) not "42"	US-PGPUB; EPO; JPO;	16:31
			DERWENT;	
			IBM_TDB	
43	4		USPAT;	2004/08/13
		not (emulat\$3 with debug\$3 with	US-PGPUB; EPO; JPO;	16:32
,		(microcontroller adj code))	DERWENT;	
	1		IBM TDB	
L				

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44	1	((((703/23-28).CCLS.)	USPAT;	2004/08/13
1 1 1	1	((((/03/23-20/.ccls.)) and @ad<20011115)	US-PGPUB;	16:36
		and (production adj microcontroller)	EPO; JPO;	10.30
		and (production ad) microcontroller,	DERWENT;	
ł	1		IBM TDB	1
45	۱ ،	((((703/23-28).CCLs.)	USPAT;	2004/08/13
1.0	ľ	((((14/30-31,34).CCLS.)) and @ad<20011115)	US-PGPUB;	16:37
		and (FPGA near2 ICE)	EPO; JPO;	10.37
		and (110A hearz 16h)	DERWENT;	
Î	İ		IBM TDB	
46	1	((((703/23-28).CCLs.)	USPAT;	2004/08/13
1	1	((714/30~31,34).CCLS.)) and @ad<20011115)	US-PGPUB;	16:40
		and (debug\$3 near (high adj frequency))	EPO; JPO;	10
		\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	DERWENT;	
ł	1		IBM TDB	
47	0	((((703/23-28).CCLS.)	USPAT;	2004/08/13
	[	((714/30-31,34).CCLS.)) and @ad<20011115)	US-PGPUB;	16:40
		and (debug\$3 adj (high adj frequency))	EPO; JPO;	
}	}		DERWENT;	
			IBM TDB	
48	60	((((703/23-28).CCLS.)	USPAT;	2004/08/13
		((714/30-31,34).CCLS.)) and @ad<20011115)	US-PGPUB;	16:41
		and (debug near interfac\$3)	EPO; JPO;	
	[		DERWENT;	
			IBM_TDB	
49	11	( ( ( ( ( ( ( ( ( ( ( ( ( ( ( ( ( ( ( (	USPAT;	2004/08/13
		((714/30-31,34).CCLS.)) and @ad<20011115)	US-PGPUB;	16:43
		and ((debug near interfac\$3) with	EPO; JPO;	
1		communicat\$3 )	DERWENT;	
L			IBM TDB	